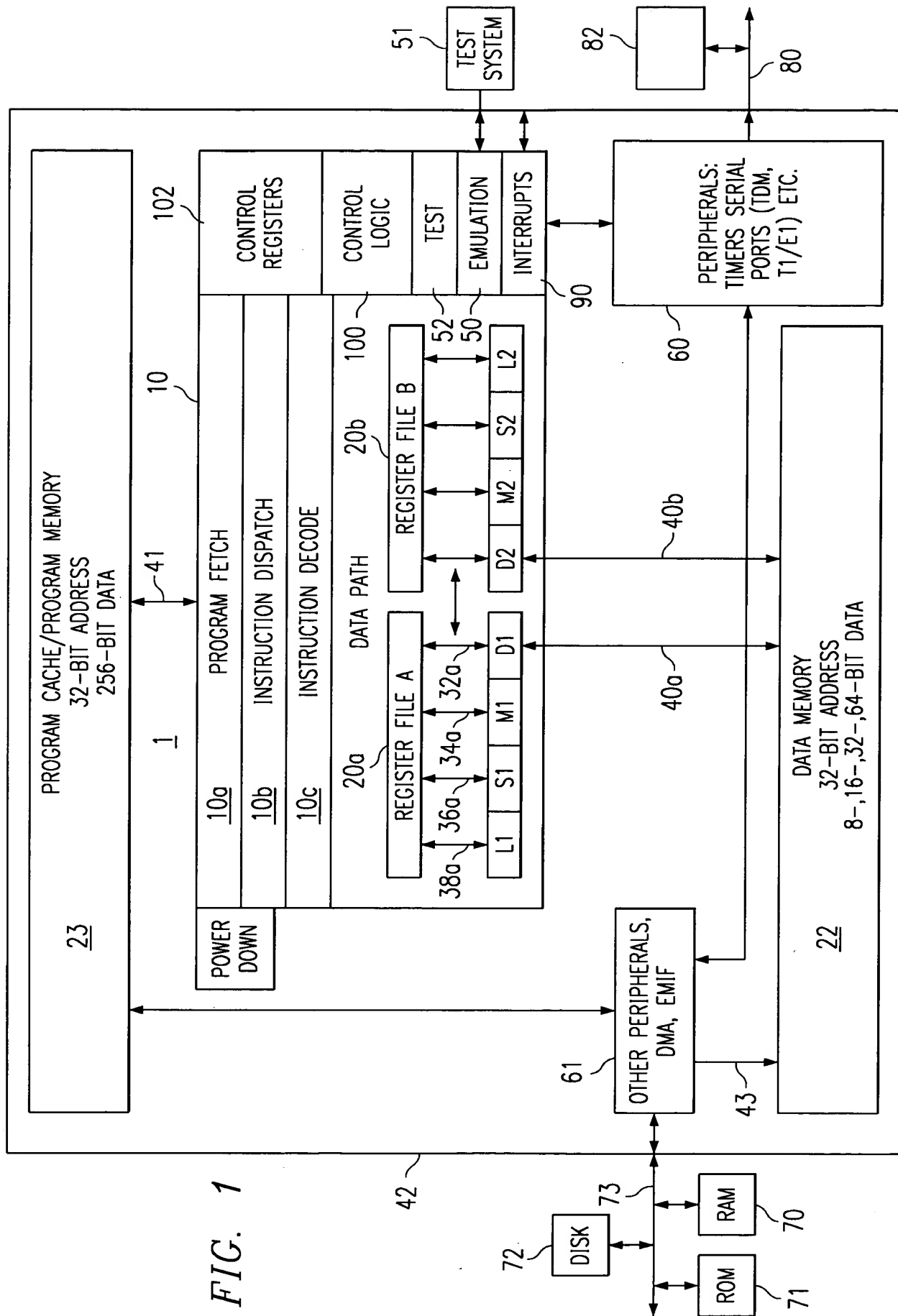


000001" 02E20460



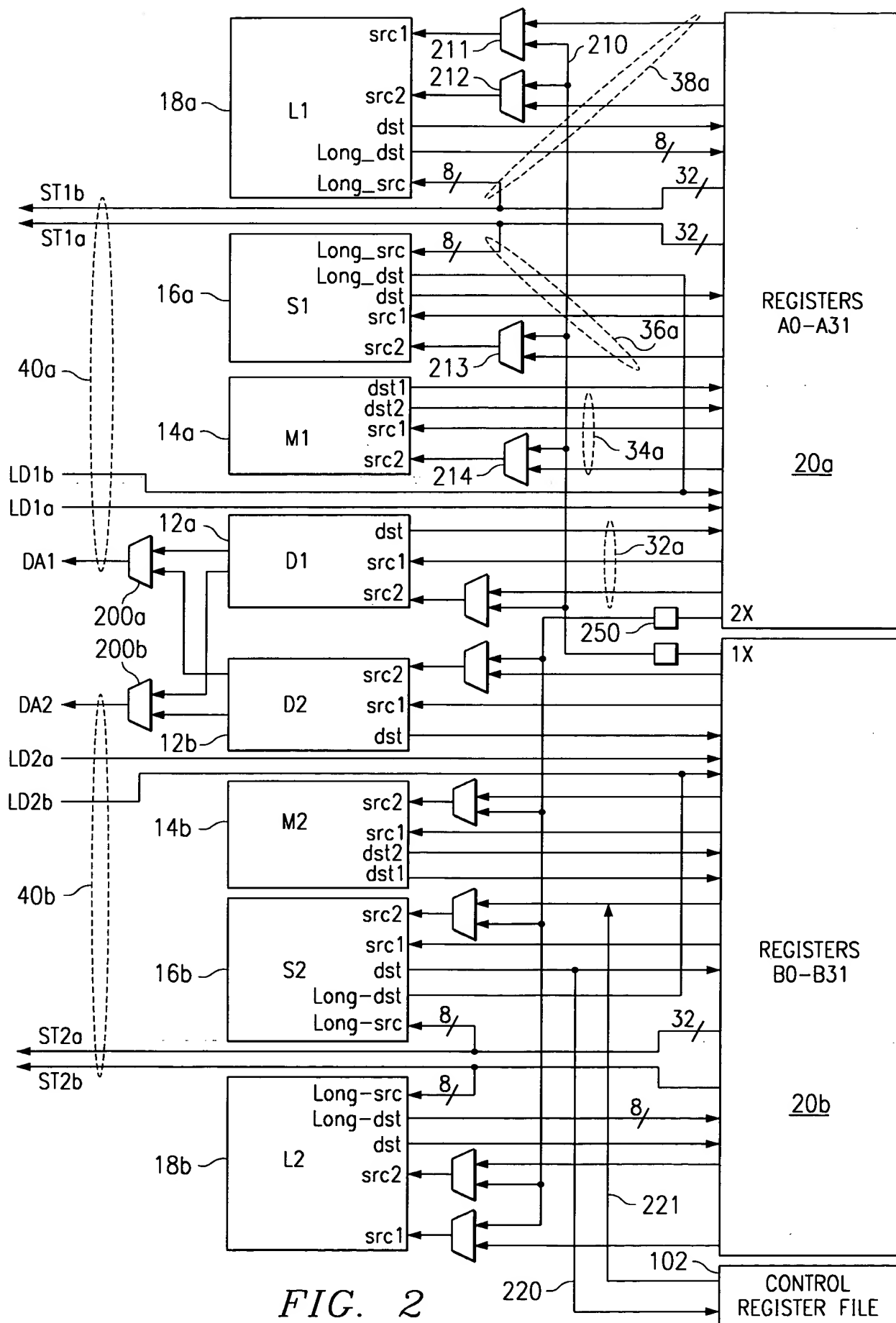


FIG. 2

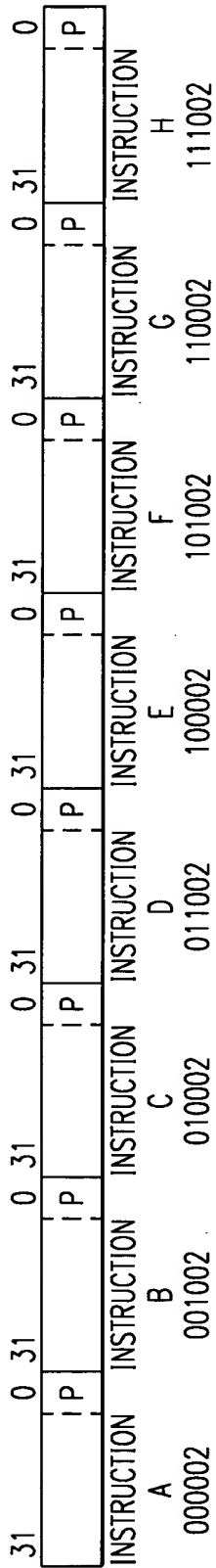


FIG. 3

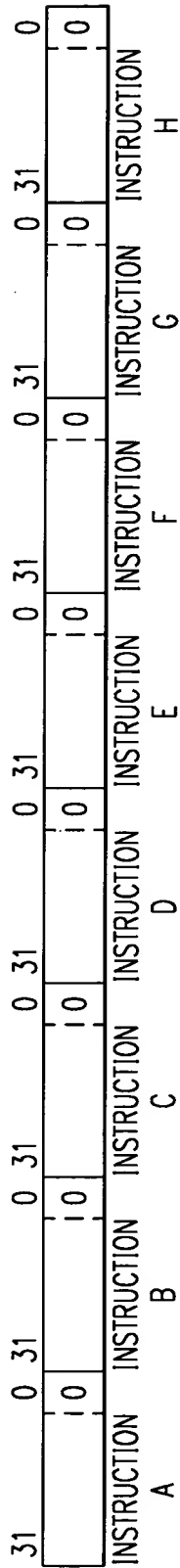


FIG. 4A

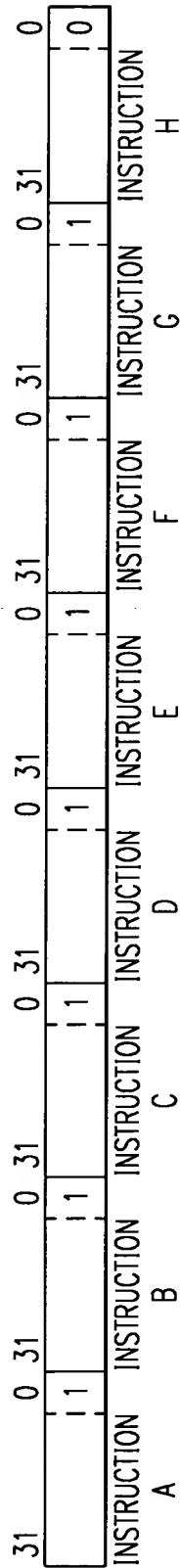


FIG. 4B

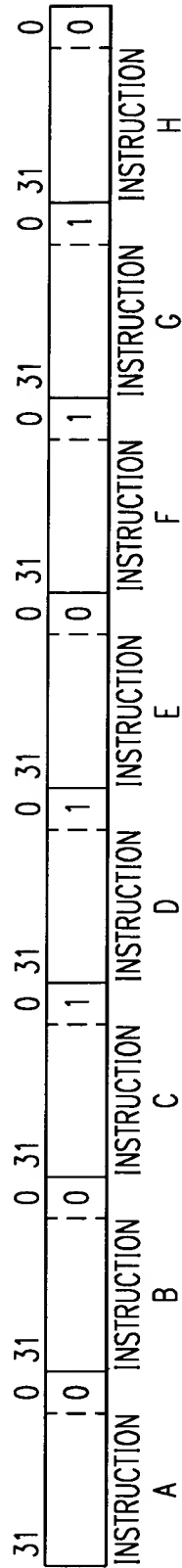


FIG. 4C

LSBs OF  
THE BYTE  
ADDRESS

		CLOCK CYCLE																						
		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23
FETCH	EXECUTE	PG	PS	PW	PR	DP	DC	E1	E2	E3	E4	E5												
PACKET	PACKET																							
n	k																							
n	k+1						DP	DC	E1	E2	E3	E4	E5											
n	k+2							DP	DC	E1	E2	E3	E4	E5										
n+1	k+3	PG	PS	PW	PR	PR	PR	PR	DP	DC	E1	E2	E3	E4	E5									
n+2	k+4			PG	PS	PW	PW	PW	PR	DP	DC	E1	E2	E3	E4	E5								
n+3	k+5				PG	PS	PS	PS	PW	PR	DP	DC	E1	E2	E3	E4	E5							
n+4	k+6					PG	PG	PG	PS	PW	PR	DP	DC	E1	E2	E3	E4	E5						
n+5	k+7								PG	PS	PW	PR	DP	DC	E1	E2	E3	E4	E5					
n+6	k+8									PG	PS	PW	PR	DP	DC	E1	E2	E3	E4	E5				

FIG. 5

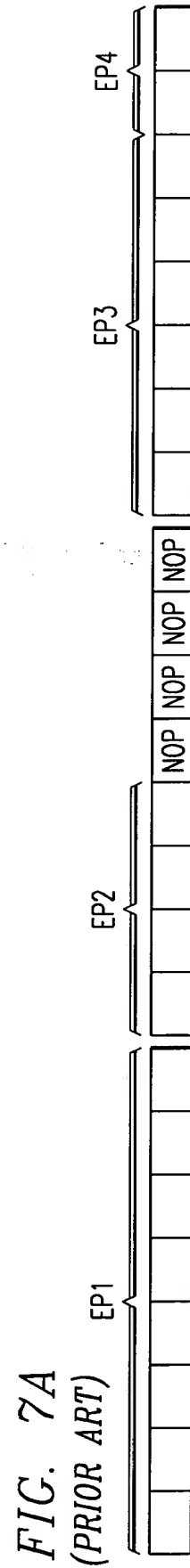


FIG. 7A  
(PRIOR ART)

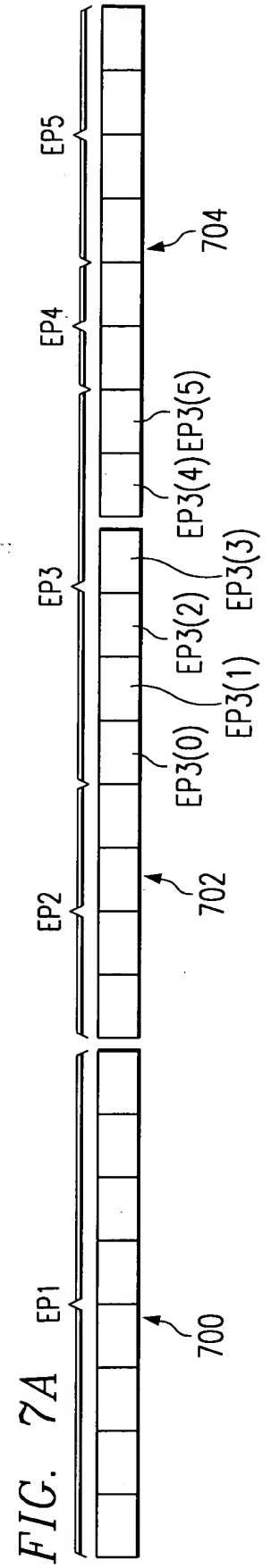
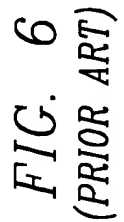


FIG. 7A





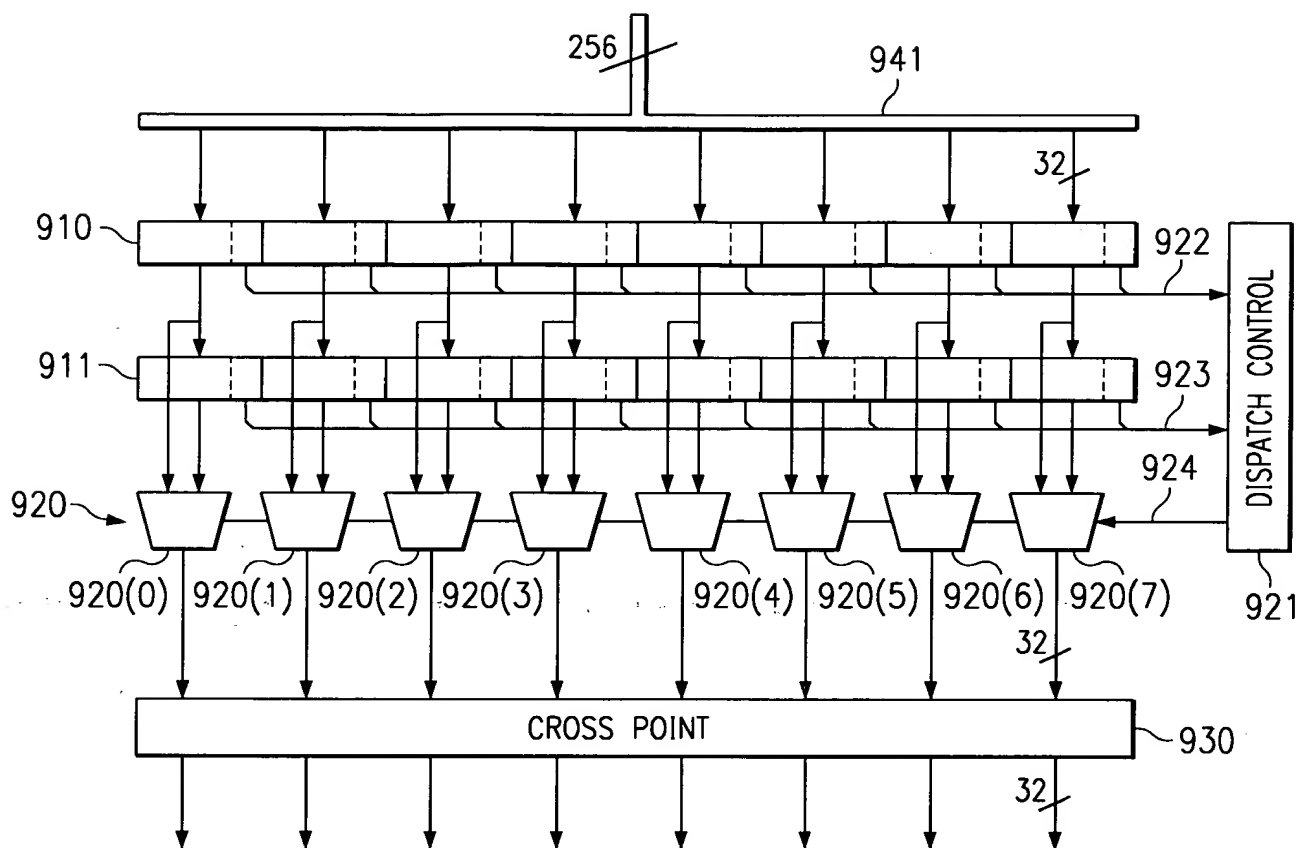


FIG. 9

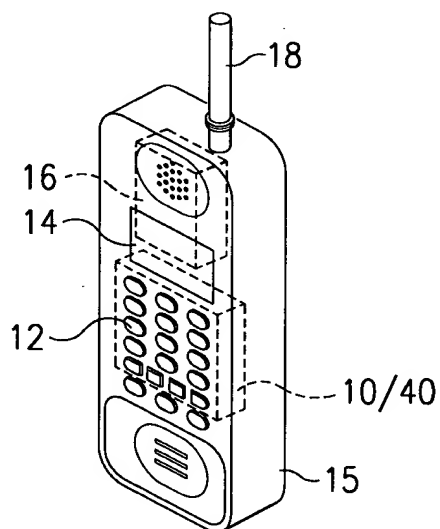


FIG. 11

0070220260

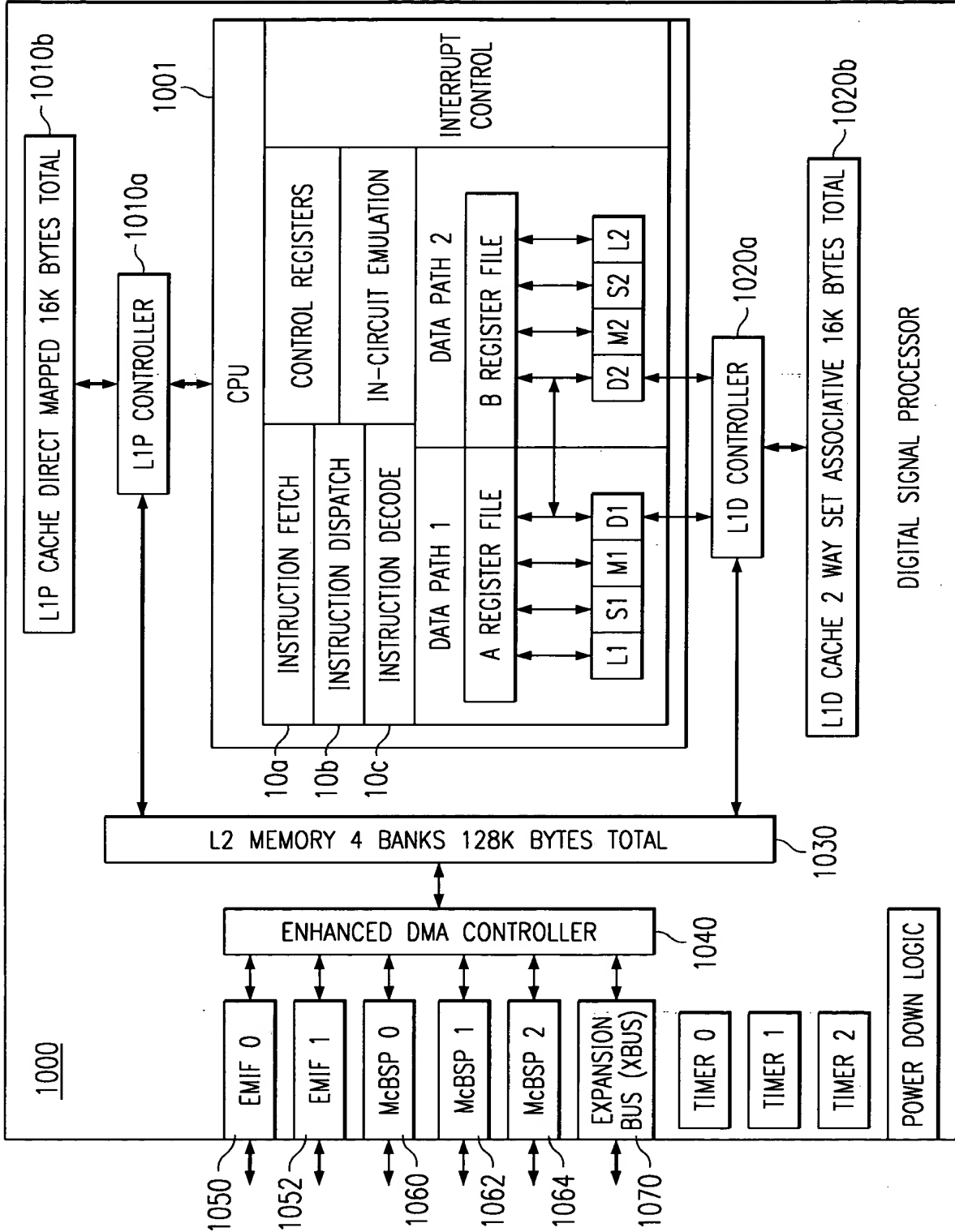


FIG. 10